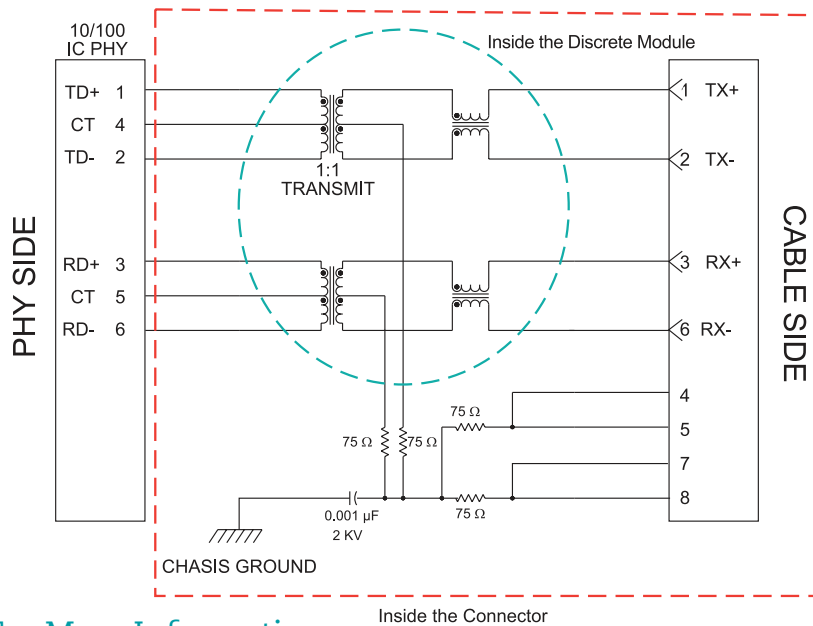




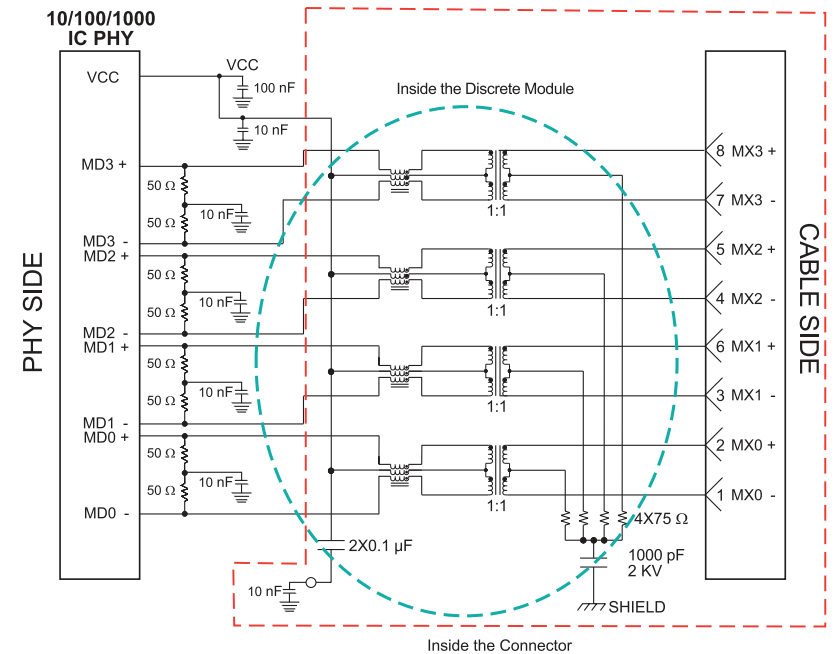
Application Notes

- Layering ground planes is advisable. Route connector/discrete module ground pins to chassis/analog ground if possible.
- Keep signal traces from PHY to connector/discrete module as short as possible. If traces exceed 3-4 inches, pay close attention to line impedance imbalance.
- Using BST (75Ω resistors and high voltage cap to chassis ground) to terminate Cable side CTs is advisable for best EMI performance (included in most connector solutions).
- Follow PHY manufacturer’s application notes for further layout

100BASE-TX



1000BASE-T



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