

FGY100T65SCDT

650 V, 100 A Field Stop Trench IGBT Short Circuit Rated IGBT

General Description

Using novel field stop IGBT technology, ON Semiconductor's new series of field stop 3rd generation IGBTs offer the optimum performance for solar, UPS, motor control, ESS and HVAC applications where low conduction and switching losses are essential.

Features

- Maximum Junction Temperature: $T_J = 175^\circ\text{C}$
- Positive Temperature Co-efficient for Easy Parallel Operating
- High Current Capability
- Low Saturation Voltage: $V_{CE(sat)} = 1.5\text{ V (Typ.) @ } I_C = 100\text{ A}$
- High Input Impedance
- Fast Switching
- Short Circuit Rated 5 μs
- Tighten Parameter Distribution
- These Devices are Pb-Free and are RoHS Compliant

Applications

- Solar, UPS, Motor Control, ESS, HVAC

ABSOLUTE MAXIMUM RATINGS (at $T_C = 25^\circ\text{C}$, Unless otherwise specified)

Symbol	Parameter	Value	Unit
V_{CES}	Collector to Emitter Voltage	650	V
V_{GES}	Gate to Emitter Voltage	± 25	V
	Transient Gate to Emitter Voltage	± 30	V
I_C	Collector Current @ $T_C = 25^\circ\text{C}$	200	A
	Collector Current @ $T_C = 100^\circ\text{C}$	100	A
I_{LM} (Note 1)	Clamped Inductive Load Current @ $T_C = 25^\circ\text{C}$	300	A
I_{CM} (Note 2)	Pulsed Collector Current	300	A
I_F	Diode Forward Current @ $T_C = 25^\circ\text{C}$ @ $T_C = 100^\circ\text{C}$	200	A
		100	A
I_{FM} (Note 2)	Pulsed Diode Maximum Forward Current	300	A
P_D	Maximum Power Dissipation @ $T_C = 25^\circ\text{C}$	750	W
	Maximum Power Dissipation @ $T_C = 100^\circ\text{C}$	375	W
T_J	Operating Junction Temperature	-55 to +175	$^\circ\text{C}$
T_{stg}	Storage Temperature Range	-55 to +175	$^\circ\text{C}$
T_L	Maximum Lead Temp. for Soldering Purposes, 1/8" from Case for 5 seconds	300	$^\circ\text{C}$
T_{SC} (Note 3)	Short circuit withstanding time @ $T_C = 150^\circ\text{C}$	5	μs

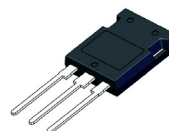
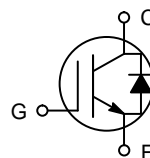
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. $V_{CC} = 400\text{ V}$, $V_{GE} = 15\text{ V}$, $I_C = 375\text{ A}$, $R_G = 10\ \Omega$, Inductive Load.
2. Repetitive rating: Pulse width limited by max. junction temperature.
3. Test condition: $V_{GE} = 15\text{ V}$, $V_{CC} = 400\text{ V}$.



ON Semiconductor®

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TO-247
CASE 340CD

ORDERING INFORMATION

See detailed ordering and shipping information on page 3 of this data sheet.

FGY100T65SCDT

THERMAL CHARACTERISTICS

Symbol	Parameter	Value	Unit
$R_{\theta JC}$ (IGBT)	Thermal Resistance, Junction to Case, Max.	0.2	$^{\circ}\text{C}/\text{W}$
$R_{\theta JC}$ (Diode)	Thermal Resistance, Junction to Case, Max.	0.3	$^{\circ}\text{C}/\text{W}$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, Max.	40	$^{\circ}\text{C}/\text{W}$

ELECTRICAL CHARACTERISTICS OF THE IGBT ($T_C = 25^{\circ}\text{C}$ unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
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OFF CHARACTERISTICS

BV_{CES}	Collector to Emitter Breakdown Voltage	$V_{GE} = 0\text{ V}, I_C = 1\text{ mA}$	650	–	–	V
$\Delta BV_{CES}/\Delta T_J$	Temperature Coefficient of Breakdown Voltage	$I_C = 1\text{ mA}$, Reference to 25°C	–	0.56	–	$\text{V}/^{\circ}\text{C}$
I_{CES}	Collector Cut-Off Current	$V_{CE} = V_{CES}, V_{GE} = 0\text{ V}$	–	–	250	μA
I_{GES}	G–E Leakage Current	$V_{GE} = V_{GES}, V_{CE} = 0\text{ V}$	–	–	± 400	nA

ON CHARACTERISTICS

$V_{GE(th)}$	G–E Threshold Voltage	$I_C = 100\text{ mA}, V_{CE} = V_{GE}$	3.5	5.3	6.9	V
$V_{CE(sat)}$	Collector to Emitter Saturation Voltage	$I_C = 100\text{ A}, V_{GE} = 15\text{ V}$	–	1.5	1.9	V
		$I_C = 100\text{ A}, V_{GE} = 15\text{ V}, T_C = 175^{\circ}\text{C}$	–	1.97	–	V

DYNAMIC CHARACTERISTICS

C_{ies}	Input Capacitance	$V_{CE} = 30\text{ V}, V_{GE} = 0\text{ V}, f = 1\text{ MHz}$	–	6310	–	pF
C_{oes}	Output Capacitance		–	384	–	pF
C_{res}	Reverse Transfer Capacitance		–	46	–	pF

SWITCHING CHARACTERISTICS

$t_{d(on)}$	Turn-On Delay Time	$V_{CC} = 400\text{ V}, I_C = 100\text{ A}, R_G = 4.7\ \Omega, V_{GE} = 15\text{ V},$ Inductive Load, $T_C = 25^{\circ}\text{C}$	–	84	–	ns
t_r	Rise Time		–	147	–	ns
$t_{d(off)}$	Turn-Off Delay Time		–	216	–	ns
t_f	Fall Time		–	133	–	ns
E_{on}	Turn-On Switching Loss		–	5.4	–	mJ
E_{off}	Turn-Off Switching Loss		–	3.8	–	mJ
E_{ts}	Total Switching Loss		–	9.2	–	mJ
$t_{d(on)}$	Turn-On Delay Time	$V_{CC} = 400\text{ V}, I_C = 100\text{ A}, R_G = 4.7\ \Omega, V_{GE} = 15\text{ V},$ Inductive Load, $T_C = 175^{\circ}\text{C}$	–	80	–	ns
t_r	Rise Time		–	160	–	ns
$t_{d(off)}$	Turn-Off Delay Time		–	244	–	ns
t_f	Fall Time		–	166	–	ns
E_{on}	Turn-On Switching Loss		–	9.7	–	mJ
E_{off}	Turn-Off Switching Loss		–	5.2	–	mJ
E_{ts}	Total Switching Loss		–	14.9	–	mJ
Q_g	Total Gate Charge	$V_{CE} = 400\text{ V}, I_C = 100\text{ A}, V_{GE} = 15\text{ V}$	–	157	–	nC
Q_{ge}	Gate to Emitter Charge		–	43	–	nC
Q_{gc}	Gate to Collector Charge		–	46	–	nC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

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ELECTRICAL CHARACTERISTICS OF THE DIODE ($T_C = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
V_{FM}	Diode Forward Voltage	$I_F = 100\text{ A}$ $T_C = 25^\circ\text{C}$ $T_C = 175^\circ\text{C}$	- -	1.68 1.45	2.1 -	V
E_{rec}	Reverse Recovery Energy	$I_F = 100\text{ A}$, $dI_F/dt = 200\text{ A}/\mu\text{s}$, $T_C = 175^\circ\text{C}$	-	96	-	μJ
t_{rr}	Diode Reverse Recovery Time	$I_F = 100\text{ A}$, $dI_F/dt = 200\text{ A}/\mu\text{s}$ $T_C = 25^\circ\text{C}$ $T_C = 175^\circ\text{C}$	- -	62 251	- -	ns
Q_{rr}	Diode Reverse Recovery Charge	$I_F = 100\text{ A}$, $dI_F/dt = 200\text{ A}/\mu\text{s}$ $T_C = 25^\circ\text{C}$ $T_C = 175^\circ\text{C}$	- -	164 2736	- -	nC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

PACKAGE MARKING AND ORDERING INFORMATION

Pare Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FGY100T65SCDT	FGY100T65SCDT	TO-247H03	Tube	-	-	30

FGY100T65SCDT

TYPICAL PERFORMANCE CHARACTERISTICS

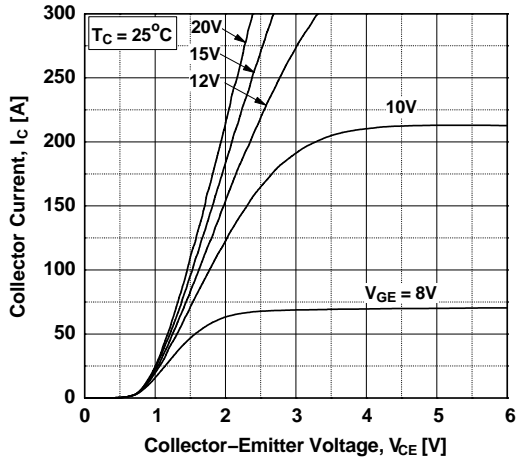


Figure 1. Typical Output Characteristics

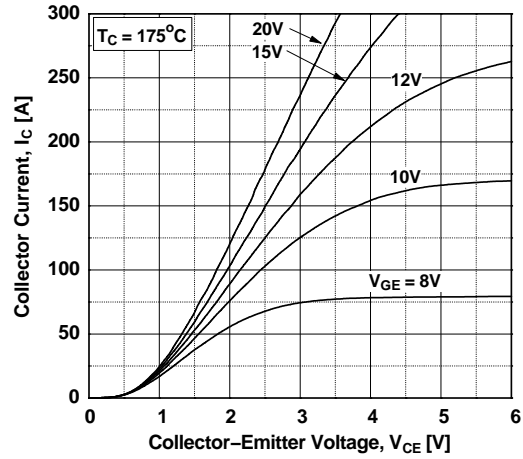


Figure 2. Typical Output Characteristics

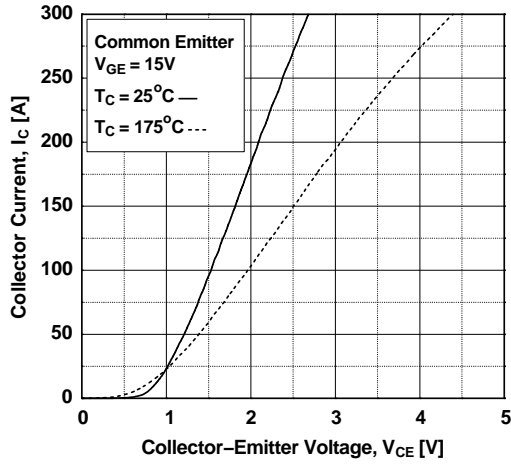


Figure 3. Typical Saturation Voltage Characteristics

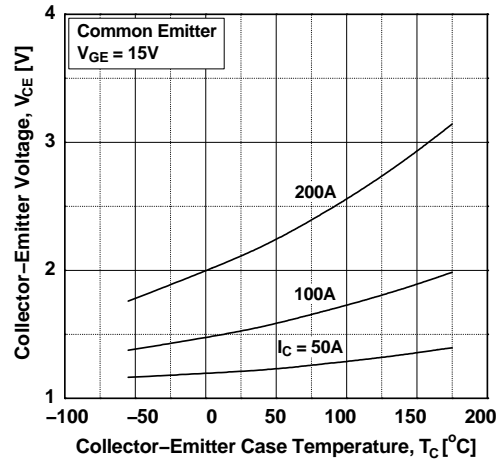


Figure 4. Saturation Voltage vs. Case Temperature at Variant Current Level

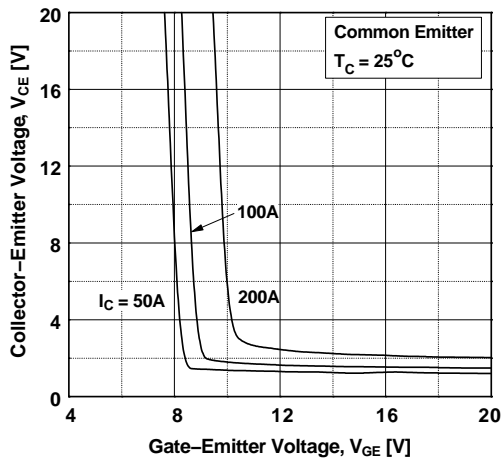


Figure 5. Saturation Voltage vs. V_{GE}

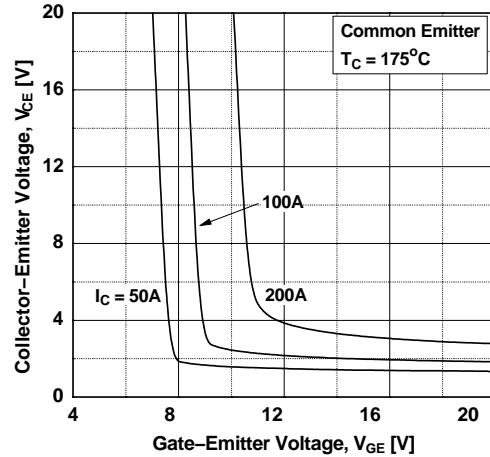


Figure 6. Saturation Voltage vs. V_{GE}

TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

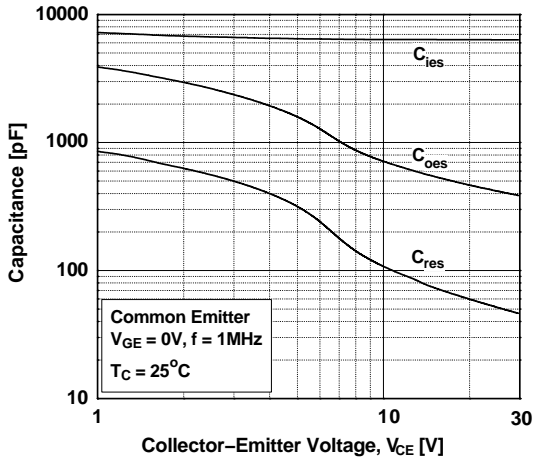


Figure 7. Capacitance Characteristics

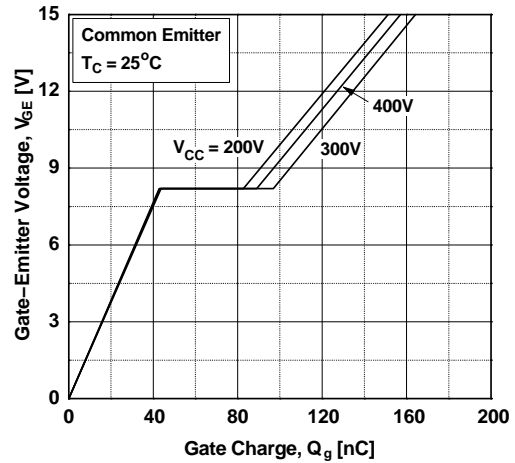


Figure 8. Gate Charge Characteristics

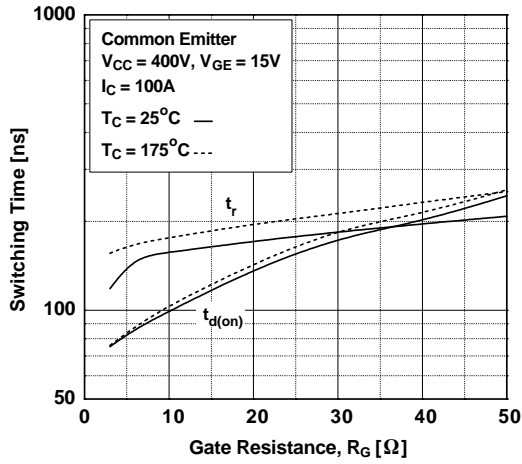


Figure 9. Turn-on Characteristics vs. Gate Resistance

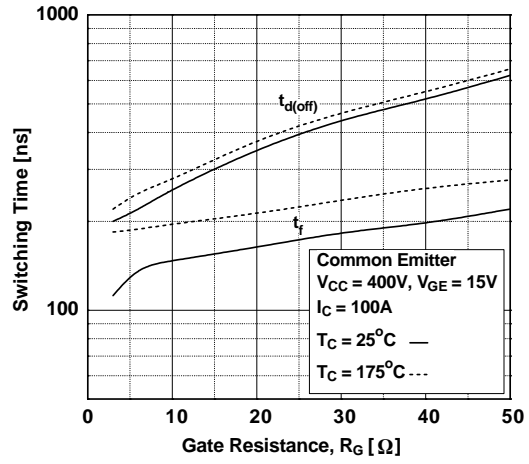


Figure 10. Turn-off Characteristics vs. Gate Resistance

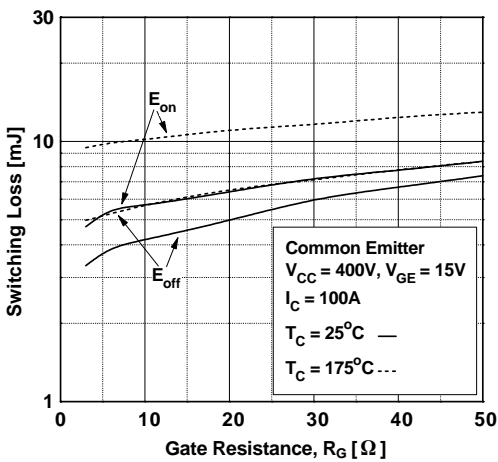


Figure 11. Switching Loss vs. Gate Resistance

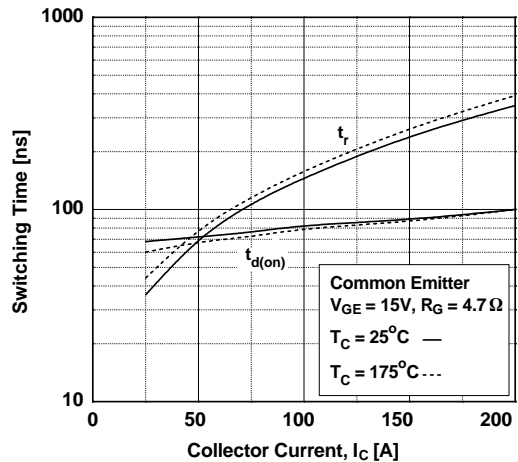


Figure 12. Turn-on Characteristics vs. Collector Current

TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

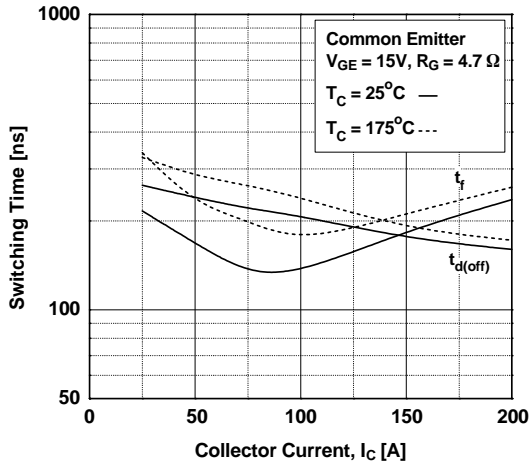


Figure 13. Turn-off Characteristics vs. Collector Current

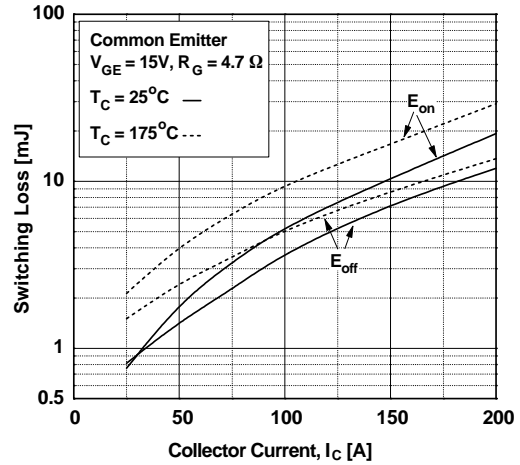


Figure 14. Switching Loss vs. Collector Current

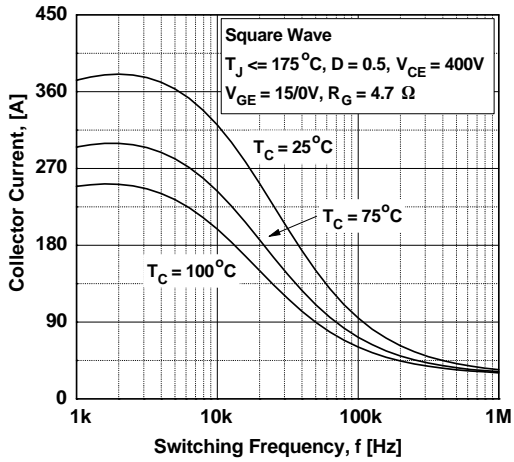


Figure 15. Load Current vs. Frequency

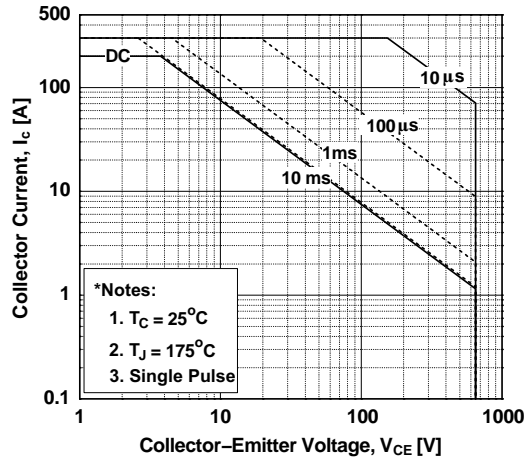


Figure 16. SOA Characteristics

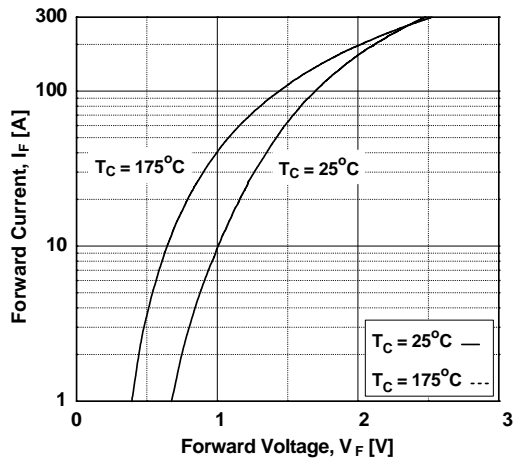


Figure 17. Forward Characteristics

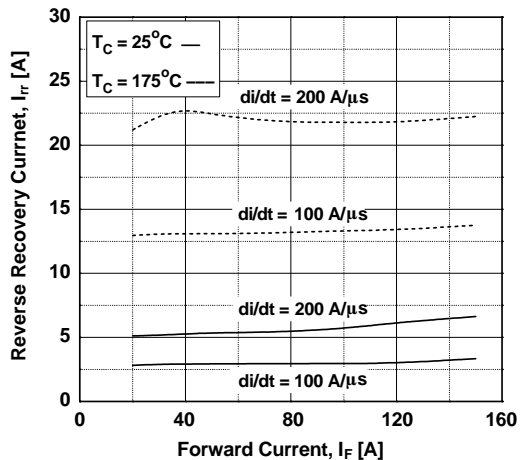


Figure 18. Reverse Recovery Current

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TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

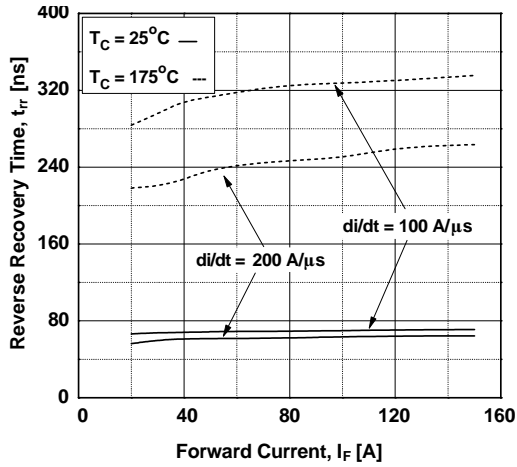


Figure 19. Reverse Recovery Time

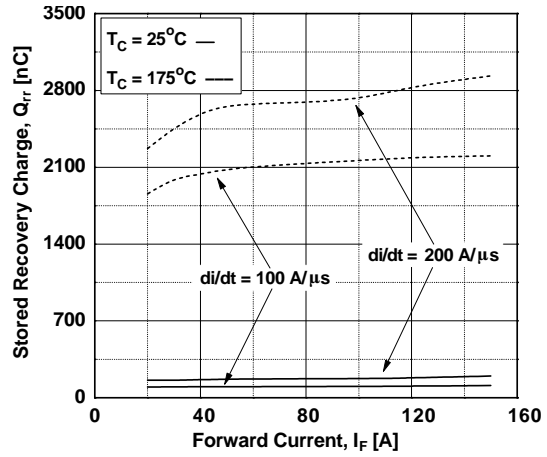


Figure 20. Stored Charge

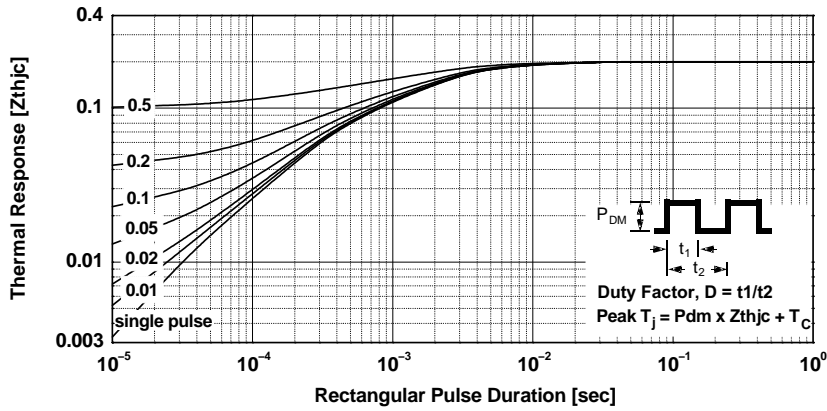


Figure 21. Transient Thermal Impedance of IGBT

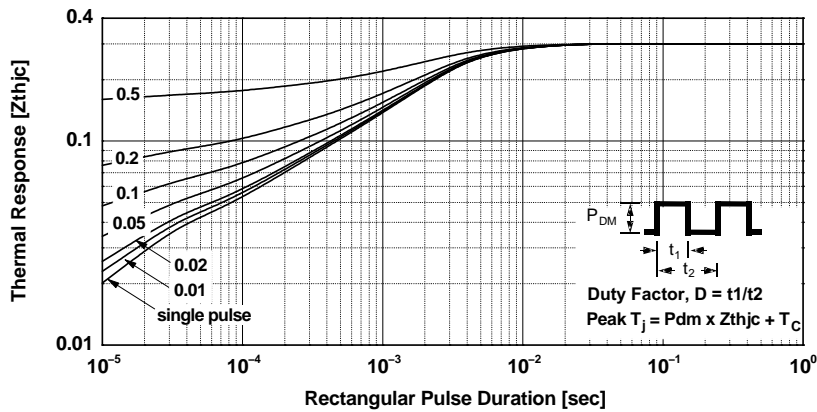
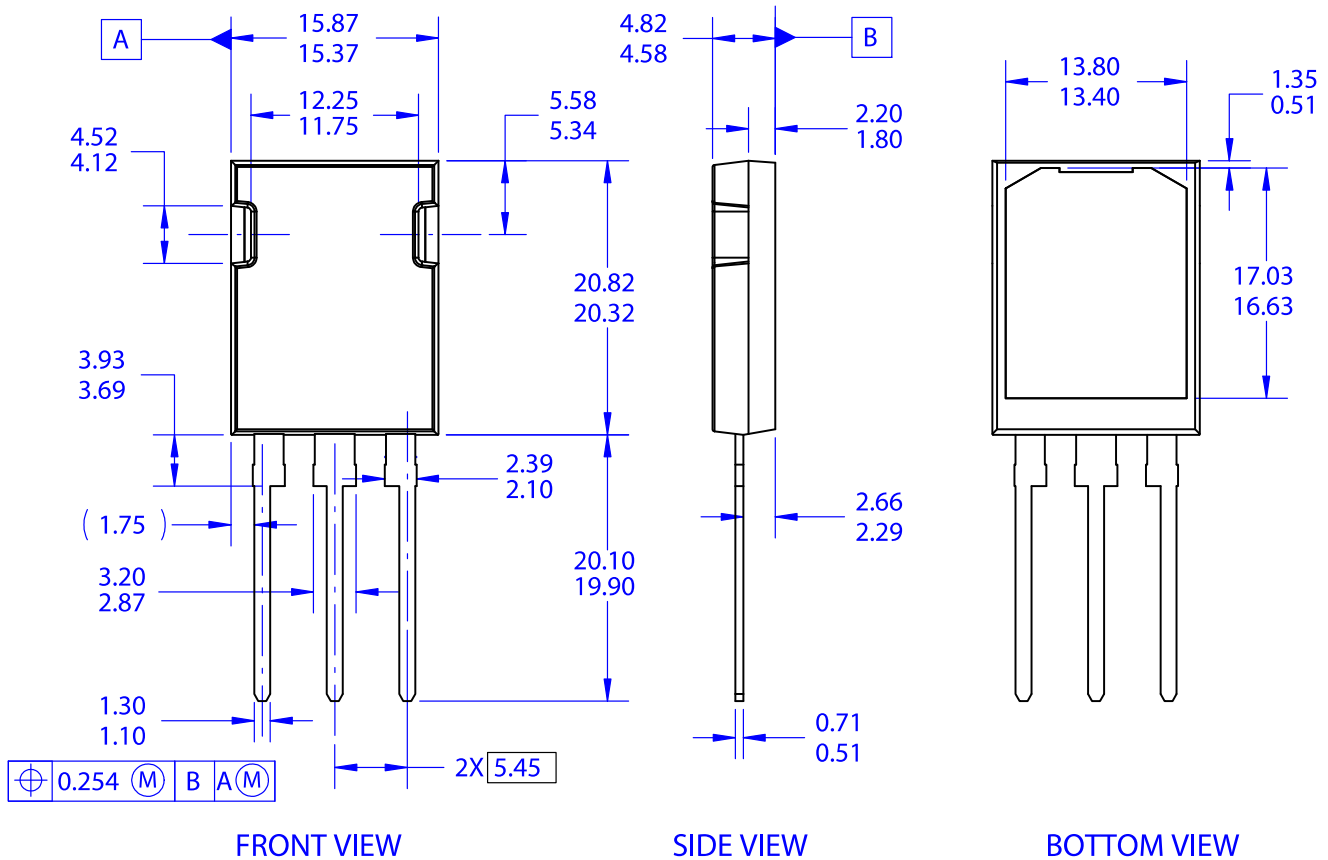


Figure 22. Transient Thermal Impedance of Diode

FGY100T65SCDT

PACKAGE DIMENSIONS

TO-247-3LD
CASE 340CD
ISSUE O



NOTES:

- A. THIS PACKAGE DOES NOT CONFORM TO ANY STANDARDS.
- B. ALL DIMENSIONS ARE IN MILLIMETERS.
- C. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH AND TIE BAR PROTRUSIONS.
- D. DIMENSION AND TOLERANCE AS PER ASME Y14.5-2009.

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