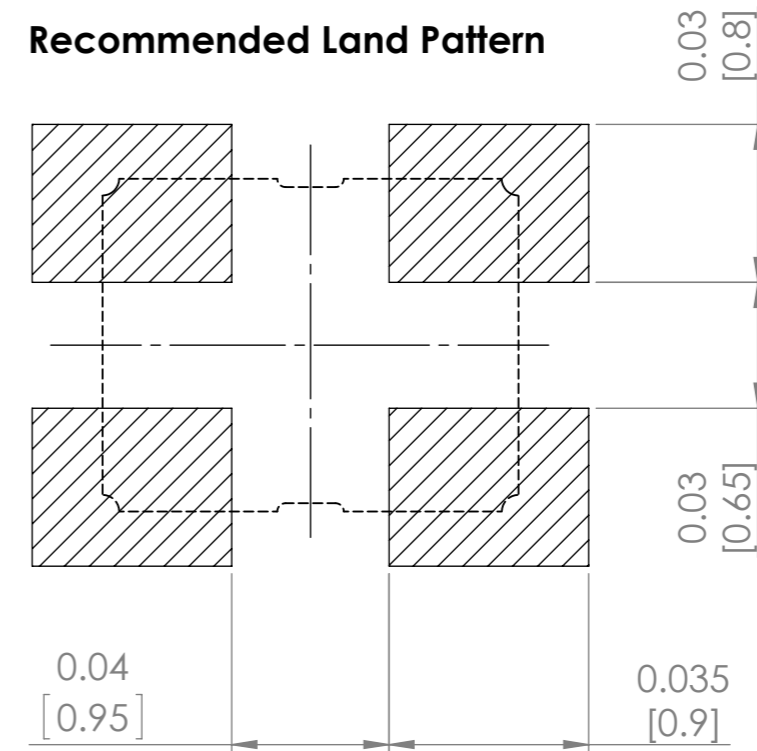
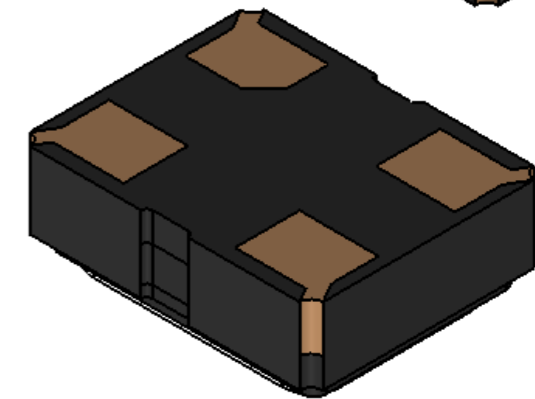
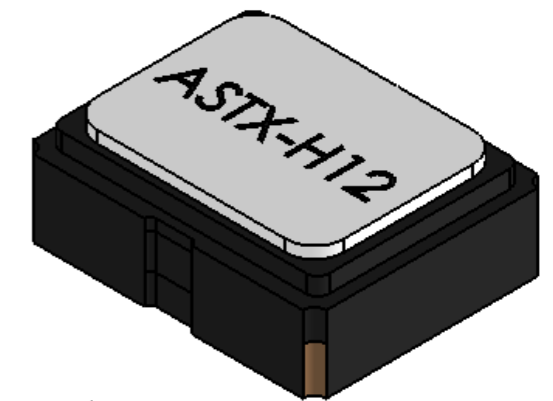


Recommended Land Pattern




Tri-state pin	Output
High or floating	Active
Low	Hi-impedance

Pin	Function
1	Tri-state
2	GND
3	Output
4	Vdd



Note: Recommend using an approximately 0.01 μF bypass capacitor between Pin 2 and 4.

UNLESS OTHERWISE SPECIFIED: DIMENSIONS ARE IN INCH(MM) SURFACE FINISH: TOLERANCES: LINEAR: ANGULAR:		FINISH:	DEBUR AND BREAK SHARP EDGES	DO NOT SCALE DRAWING	REVISION -
NAME	SIGNATURE	DATE		 30332 Esperanza, Rancho Santa margarita, California 92688 ABRACON CORPORATION TITLE: Oscillator DWG NO. ASTX-H12	
DRAWN SAAZVAT					
CHK'D XXXXXX					
APPV'D					
MFG					
Q.A			MATERIAL:	SCALE:22:1	A3
			WEIGHT:	SHEET 1 OF 1	